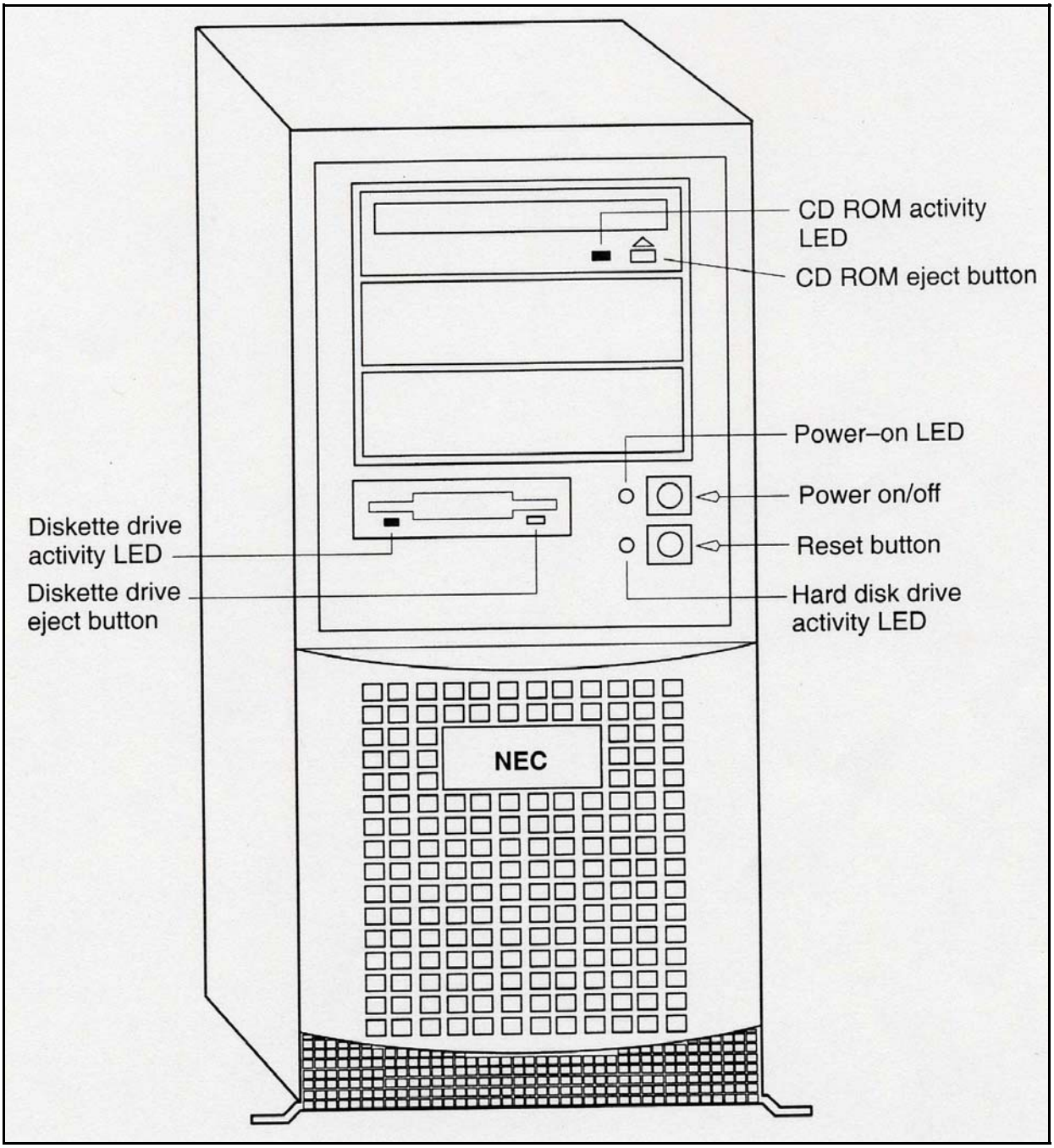

Figure A-3 Front-End Processor–Front View